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## **CLAIMS**

1.	A booting system comprising:		
a processor;			
a first boot device coupled to said processor; and			
a secoi	nd boot device coupled to said proc	essor.	

The system of claim 1 further comprising:

- a switching mechanism configured to cause said processor to become disassociated with said first poot device and associate with said second boot device when it is determined that said first poot device is inoperable.
- 3. The system of claim the further comprising:

  a mirroring mechanism wherein a first data on said first boot device is copied to said second boot device.
  - 4. The system of claim 3, wherein said switching mechanism is further configured to allow removal of said first or second boot device.
- 5. The system of claim 3, wherein said mirroring mechanism copies said first data to said second boot device whenever said first data is modified.
  - 6. The system of claim 3, wherein said first data is an operating system.
  - 7. The system of claim 6, wherein said operating system is UNIX.

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	8.	The system of claim 6, wherein said operating system is Windows.
	9.	The system of claim 6, wherein said operating system is Mac OS.
	10.	The system of claim 1 further comprising:
	a first j	port controller coupling said processor to said first and second boot devices
and	a secon	nd port controller coupling said processor to said first and second boot
device		
	11.	The system of claim 10 further comprising:
	a routi	ng mechanism wherein a communication between said processor and said
first or	second	boot device is transmitted through said first port controller and said second
port co	ontroller	
	12.	The system of claim 1 further comprising:
	a first	power source; and
	a secon	nd power source.

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- The system of claim 12, wherein said first power source or said second 13. power source can power said system.
  - A method of booting a computer system comprising: 14.
- 25 providing a processor;

providing a first boot device coupled to said processor;

providing a second boot device coupled to said processor; and

determining whether to associate said processor with said first or second boot device.

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15. The method of claim 14, further comprising: removing said first boot device or said second boot device from said system; initiating an operating system from said removed boot device if necessary.

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16. The method of claim 14, further comprising: mirroring said first data on said first and second boot devices.

The method of claim 14, further comprising:

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replacing said first or second boot devices with a new boot device when said first or second boot device fails.

18. The method of claim 14, further comprising:

providing a first port controller coupling said processor to said first and second

20 boot devices;

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providing a second port controller coupling said processor to said first and second boot devices; and

routing data accesses from said processor to said first or second boot devices through said first of second port controllers.

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19. The method of claim 14, further comprising:

powering said computer system using either a first power source or a second

power source.

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